

Patented 02-25-2008

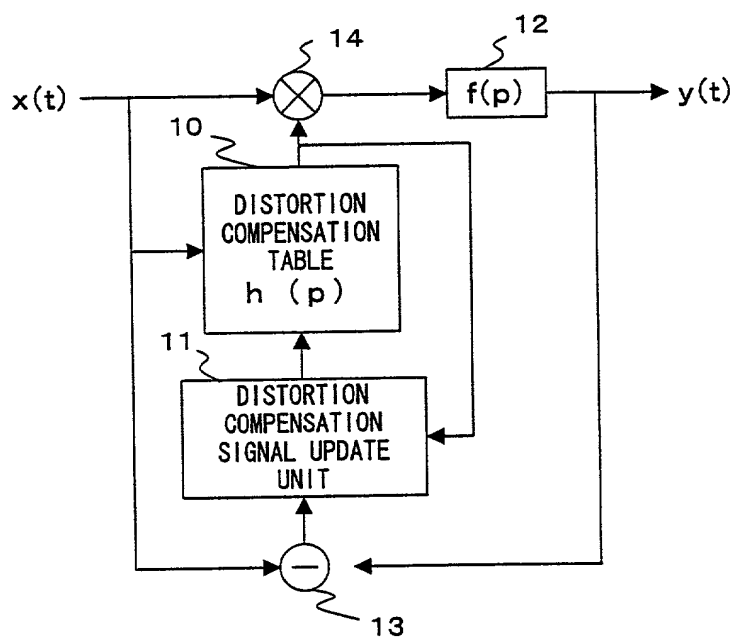


FIG. 1

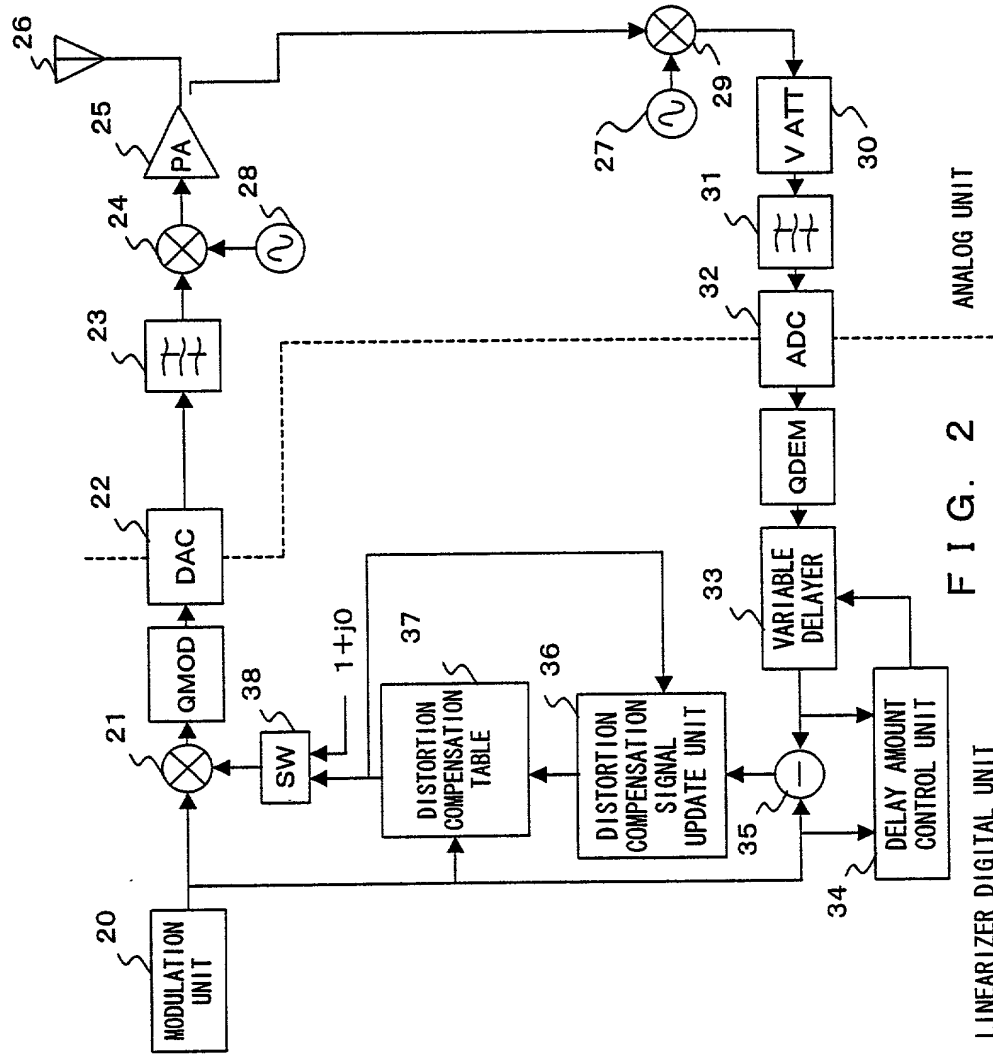


FIG. 2

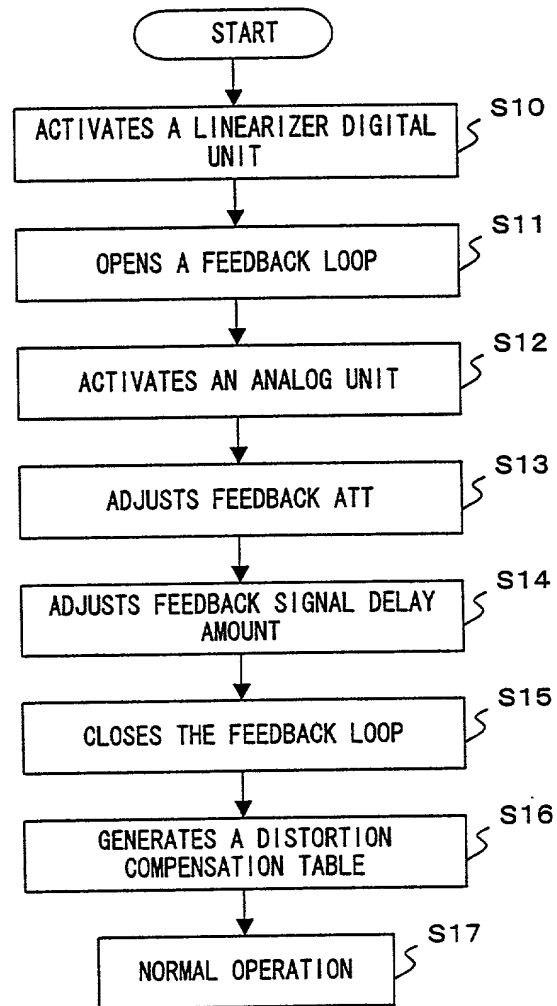


FIG. 3

FIG. 4

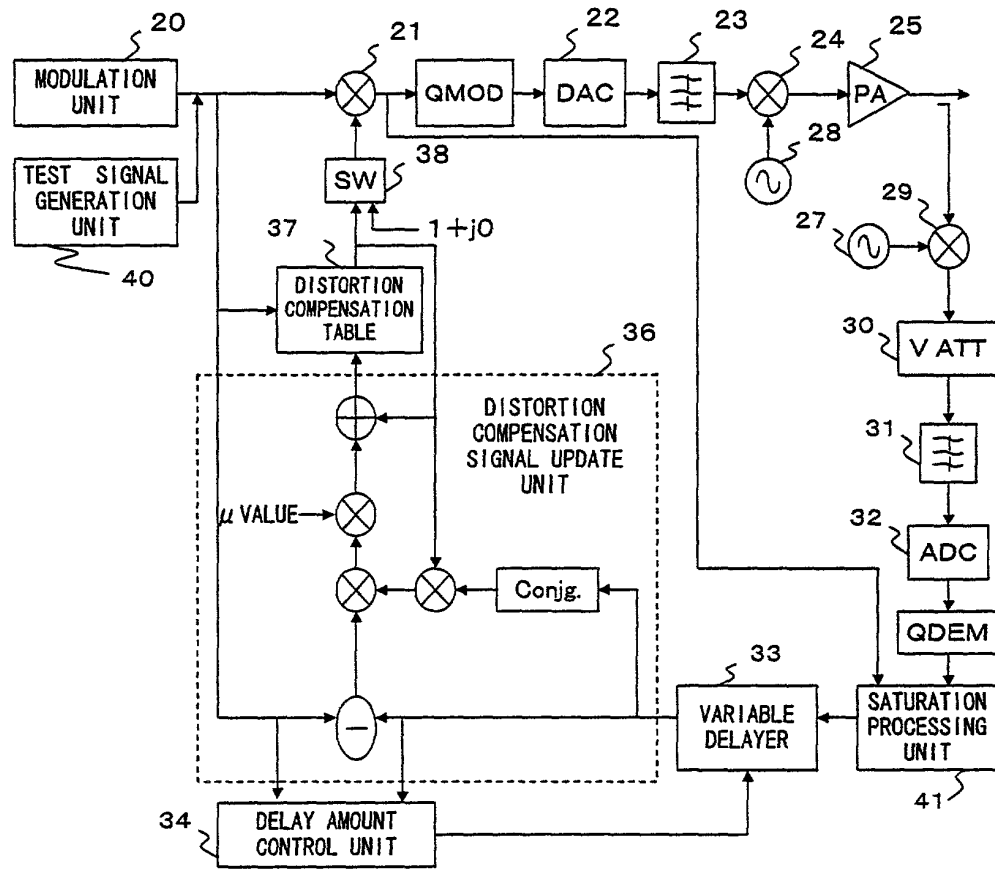


FIG. 4

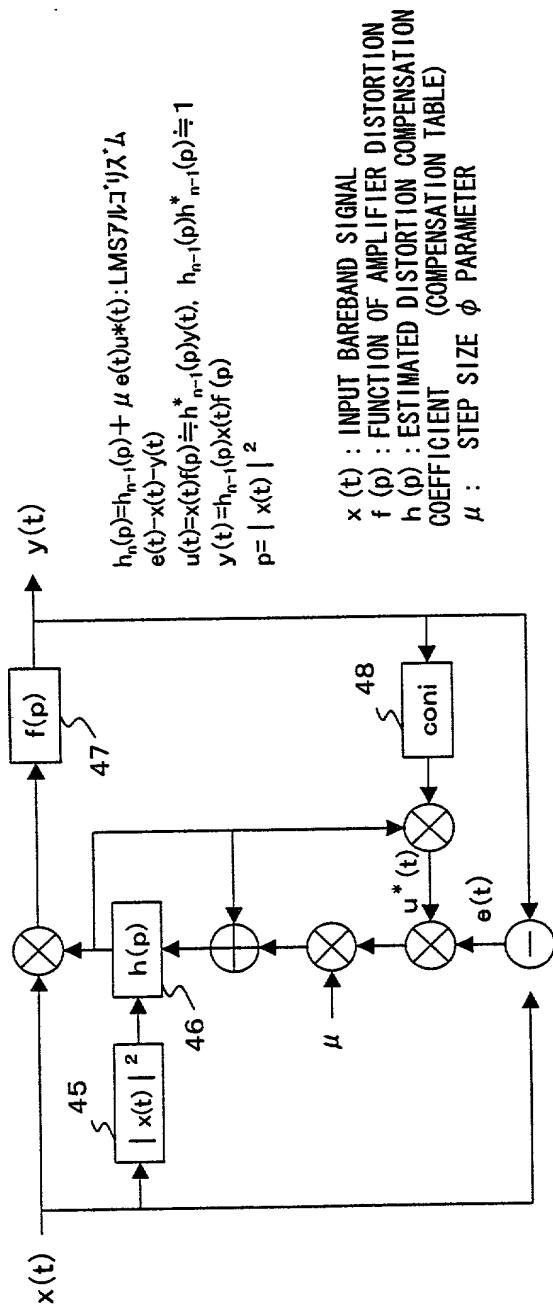


FIG. 5

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graph TD
    START([START]) --> S20[ACTIVATES A LINEARIZER DIGITAL UNIT]
    S20 --> S21[OPENS A FEEDBACK LOOP]
    S21 --> S22[ACTIVATES AN ANALOG UNIT  
GENERATES A TEST SIGNAL (AT A SET LEVEL)]
    S22 --> S23[ADJUSTS FEEDBACK ATT]
    S23 --> S24[ADJUSTS A FEEDBACK SIGNAL DELAY AMOUNT]
    S24 --> S25[SETS A DISTORTION COMPENSATION TABLE TO  
AN INITIAL VALUE]
    S25 --> S26[FEEDBACK LOOP]
    S26 --> S27[CONTINUES TO CHANGE A TEST SIGNAL LEVEL SO  
THAT A DISTORTION COMPENSATION COEFFICIENT  
CAN BE GENERATED FOR EACH OF ALL THE  
ADDRESSES OF THE DISTORTION COMPENSATION  
TABLE]
    S27 --> S28[UPDATES THE DISTORTION COMPENSATION  
COEFFICIENT IN A SPECIFIC ADDRESS]
    S28 --> S29{IS THE COMPENSATION COEFFICIENT  
CONVERGED?}
    S29 -- Y --> S32{IS A DISTORTION COMPENSATION  
COEFFICIENT GENERATED FOR  
EACH OF ALL THE ADDRESSES  
GENERATED?}
    S29 -- N --> S30{IS THE COMPENSATION PROCESS  
REQUIRED?}
    S30 -- Y --> S31[SATURATION PROCESS]
    S31 --> S28
    S30 -- N --> S28
    S32 -- Y --> S33[STOPS THE GENERATION OF  
A TEST SIGNAL]
    S32 -- N --> S28
    S33 --> S34[NORMAL OPERATION]
  
```

FIG. 6

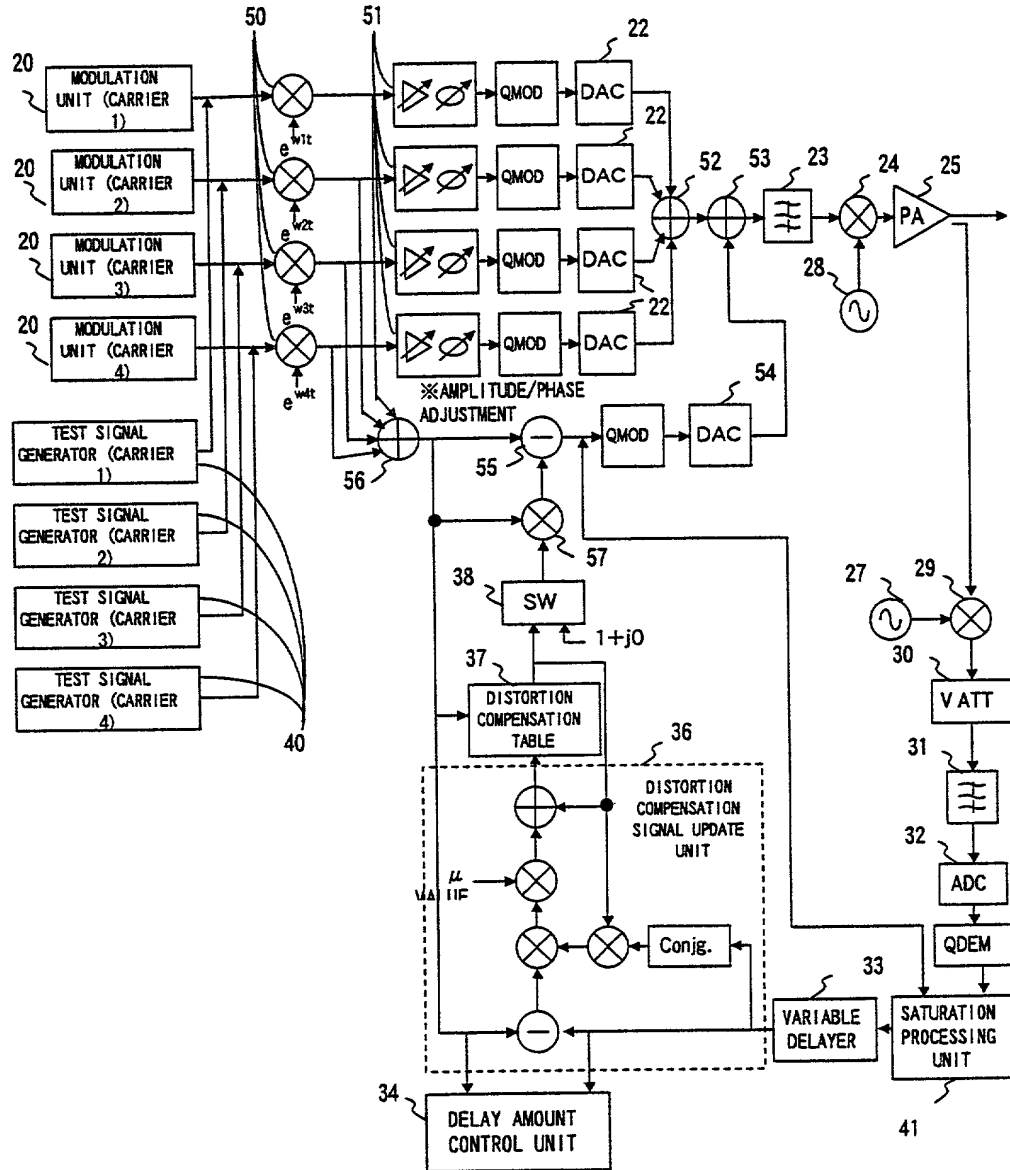


FIG. 7

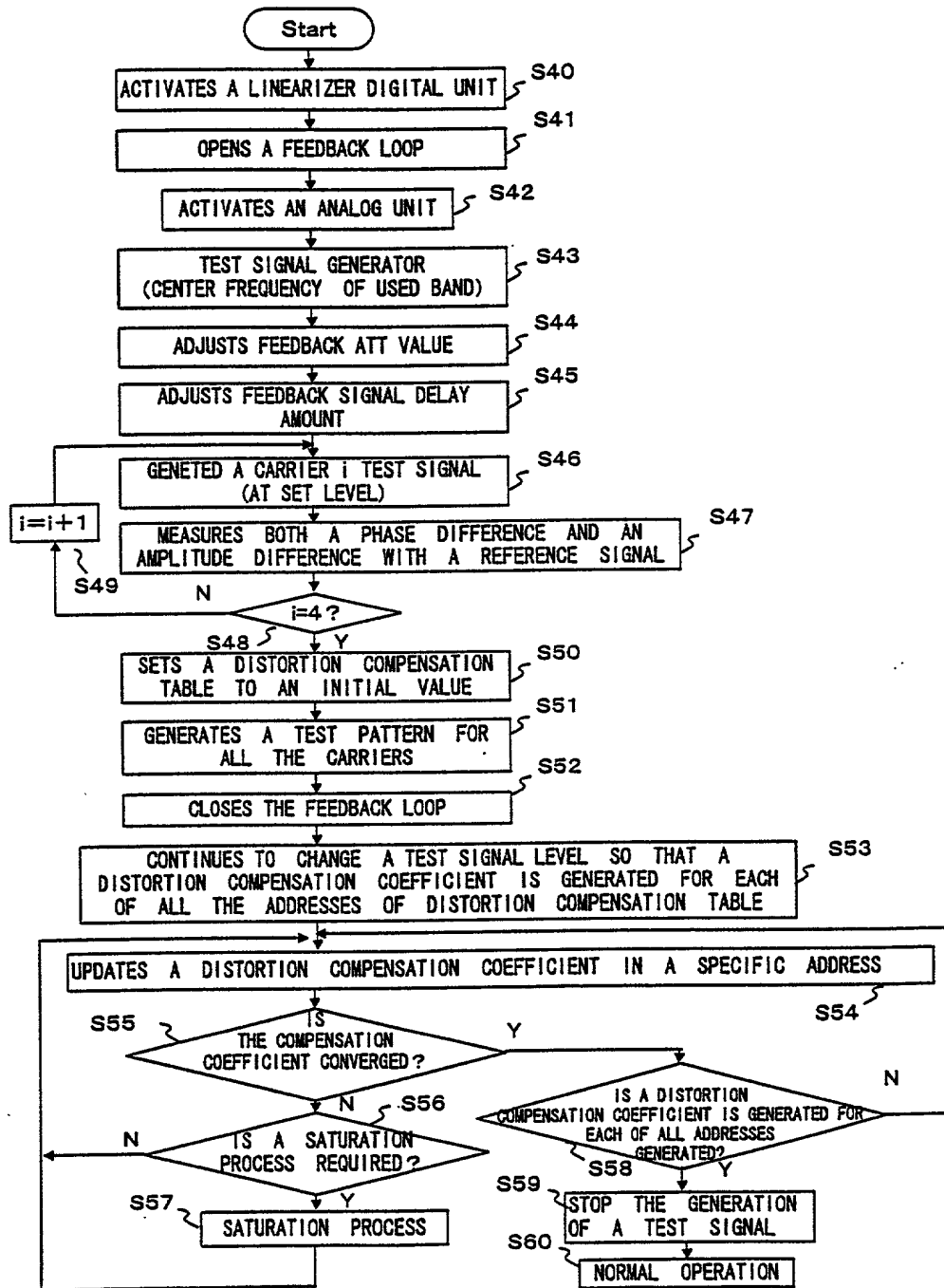


FIG. 8



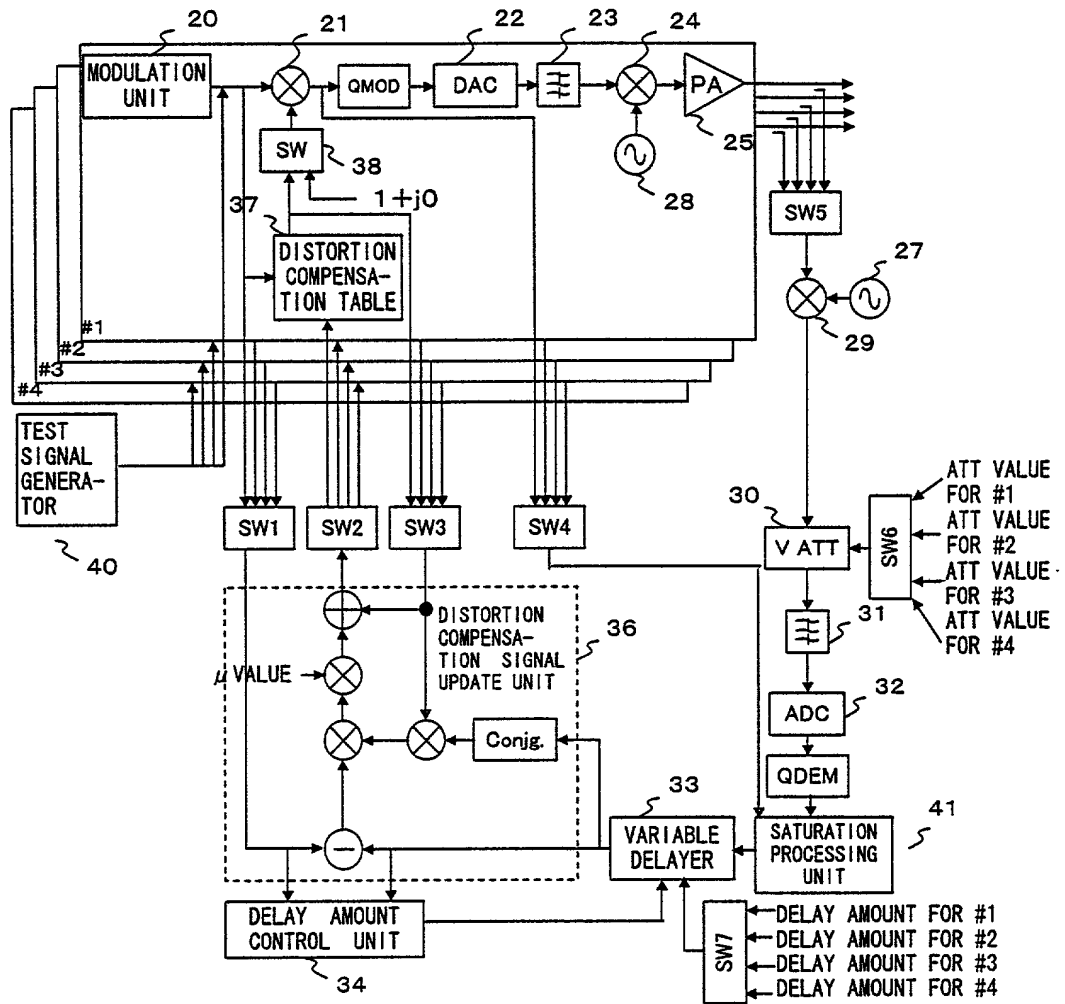


FIG. 9

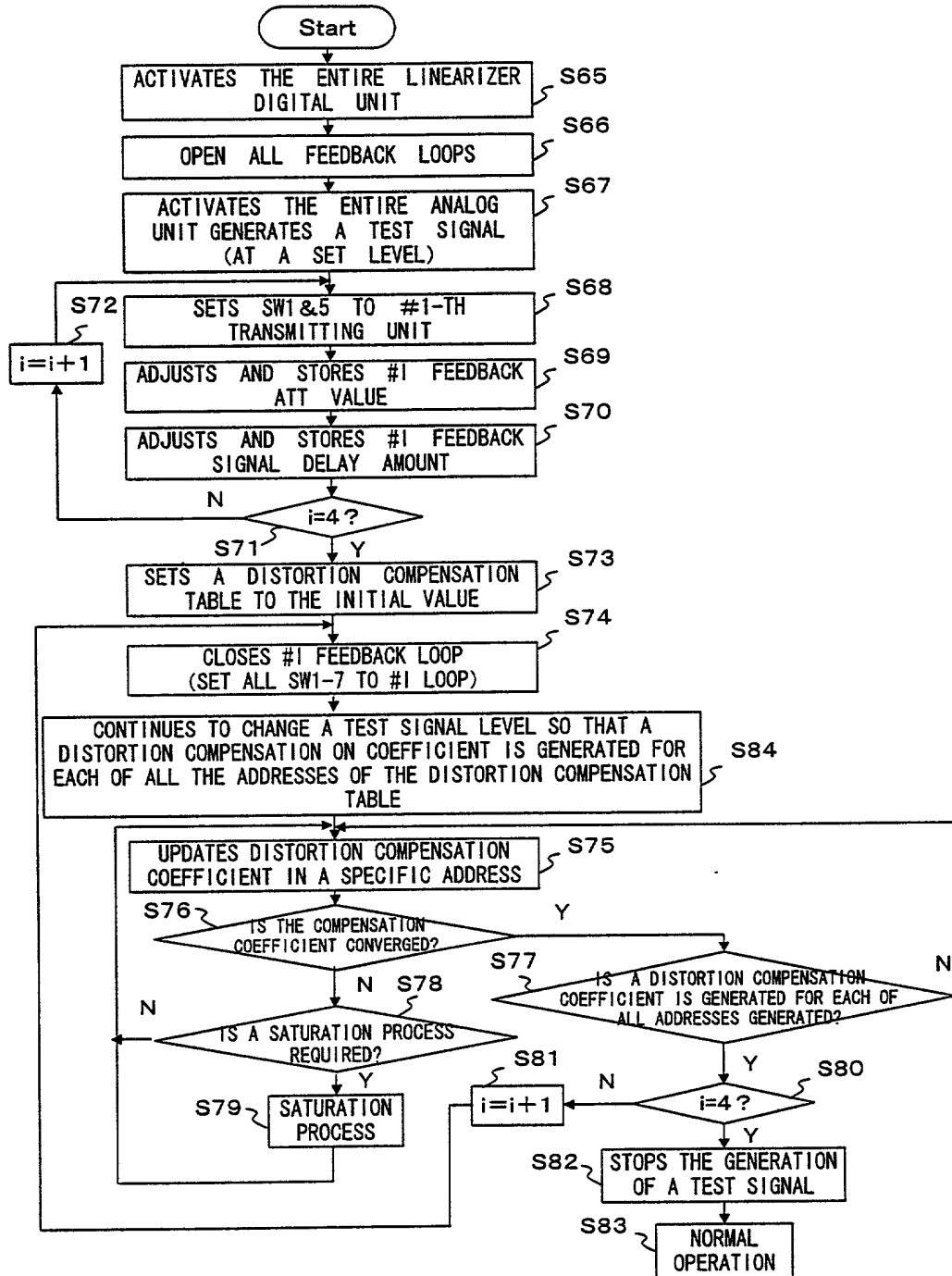
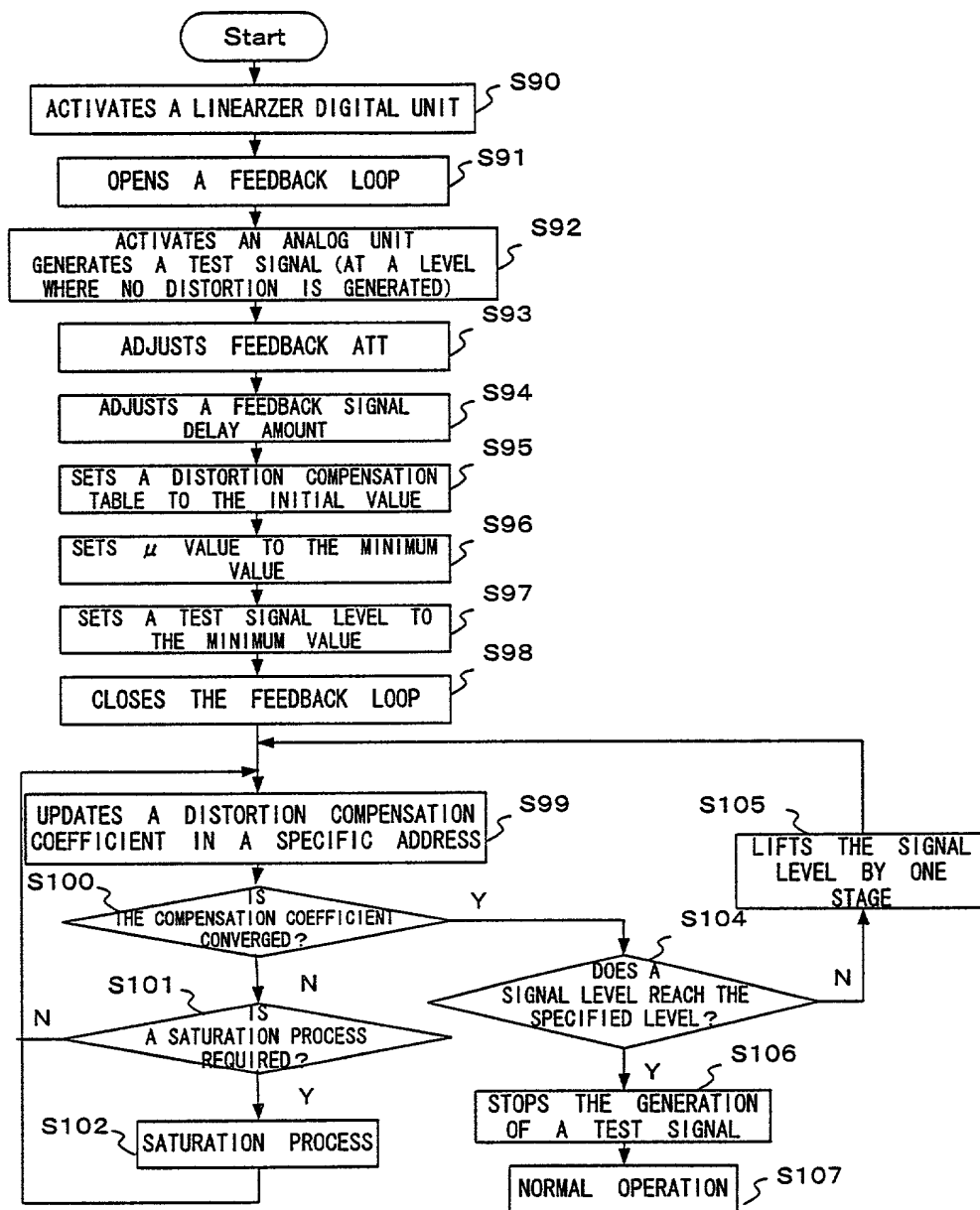


FIG. 10



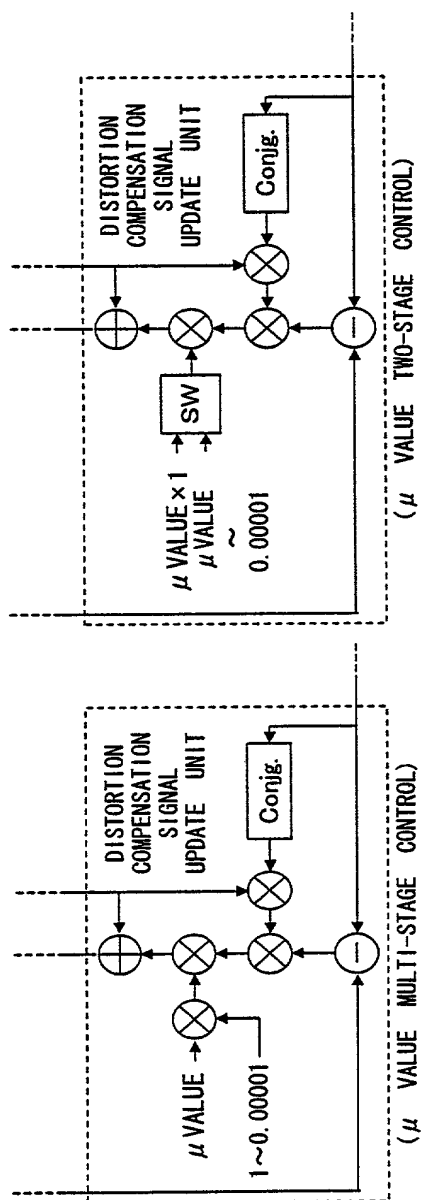


FIG. 12A

FIG. 12B

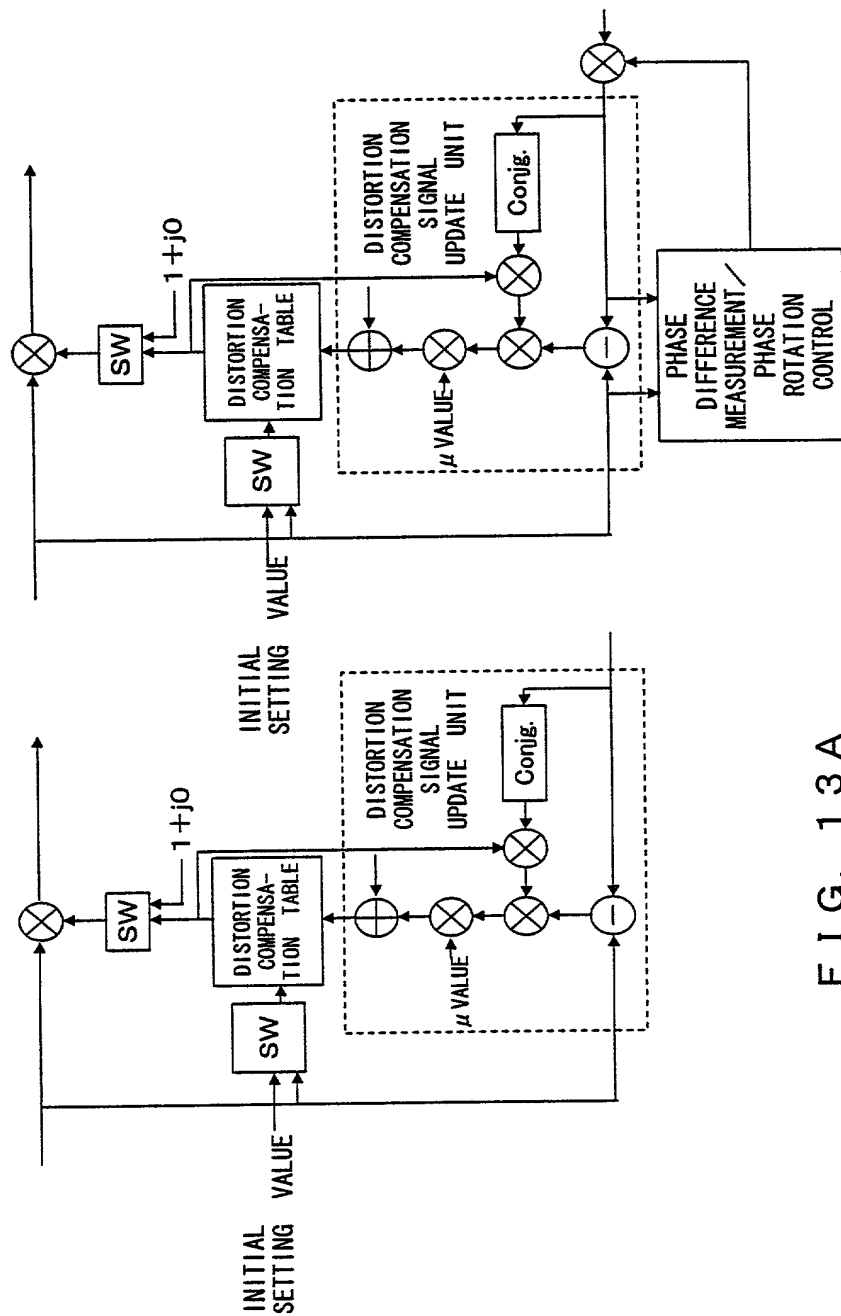
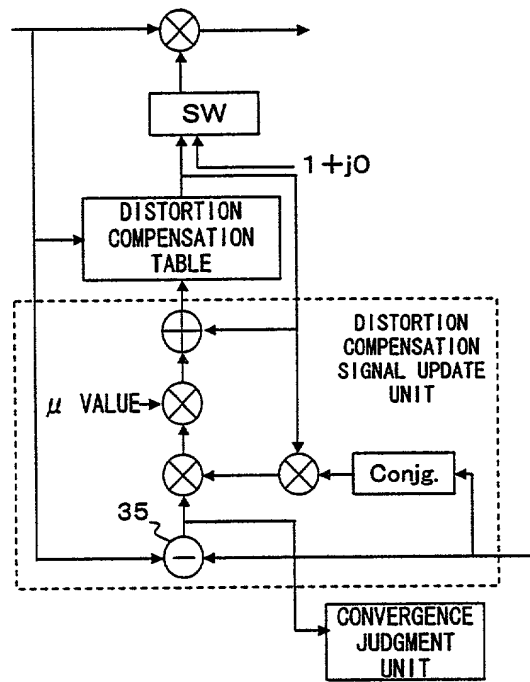


FIG. 13A

FIG. 13B



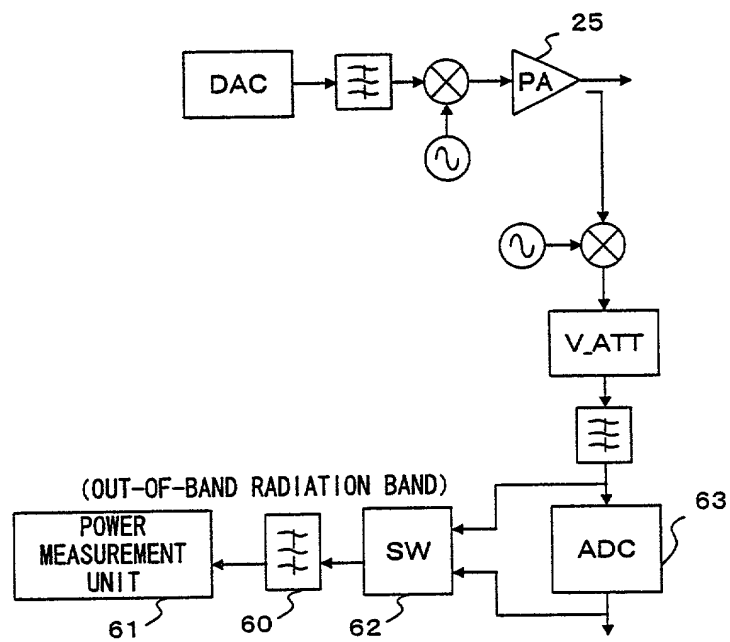


FIG. 15

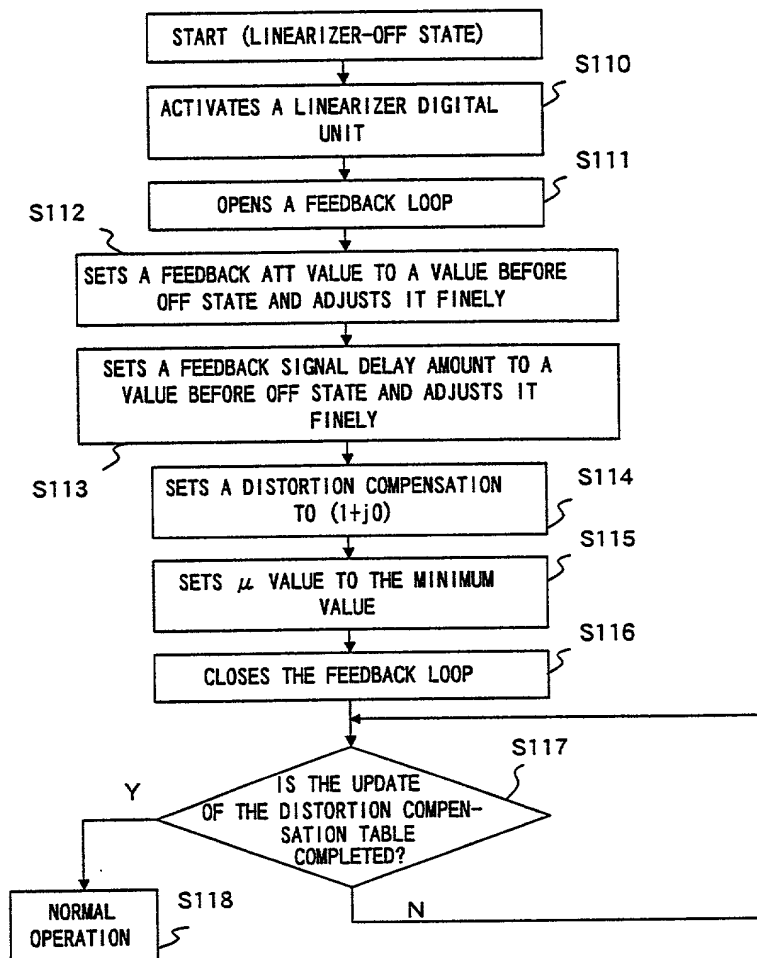


FIG. 16



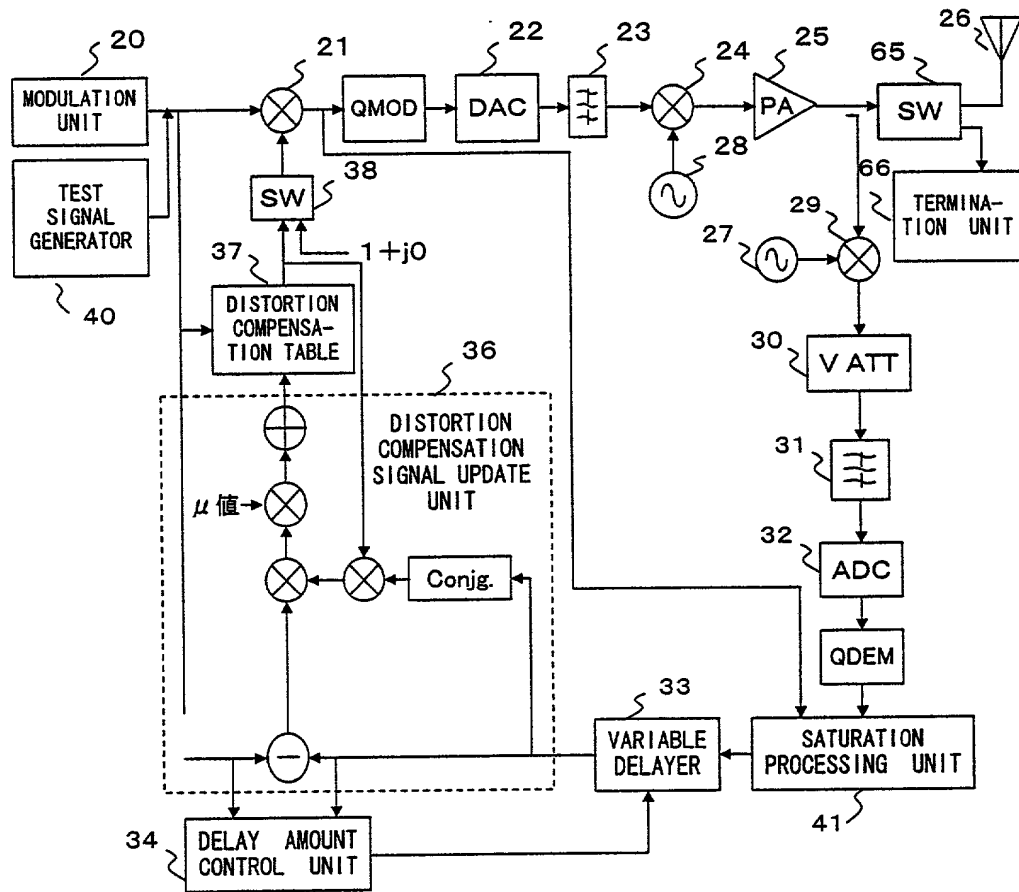


FIG. 17